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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Taketomi ASAMI et al.

Serial No. 09/842,315 ✓

Filed: April 26, 2001 ✓

For: MANUFACTURING METHOD  
FOR SEMICONDUCTOR  
DEVICE

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Art Unit: 2813

Examiner: D. Blum

## CERTIFICATE OF MAILING

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*William D. Stanger*AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated February 13, 2003, please amend the  
above-identified application as follows:

IN THE CLAIMS:

Please amend claims 1-15 as follows:

1. (Amended) A method for manufacturing a semiconductor device  
comprising:

the first step of forming a semiconductor film over a substrate;

the second step of forming a thin oxide film on a surface of the semiconductor  
film by applying water in which ozone is dissolved;

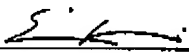
the third step of etching the semiconductor film to remove the thin oxide film and  
contaminant impurities on the surface of the semiconductor film by applying an etching  
solution while spinning the substrate; and

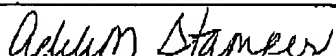
the fourth step of forming a gate insulating film in contact with the semiconductor  
film after the third step,

PTO/SB/21 (08-00)

<b>TRANSMITTAL FORM</b>  <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/842,315
	Filing Date	April 26, 2001
	First Named Inventor	Taketomi ASAMI et al.
	Group Art Unit	2813
	Examiner Name	D. Blum
Total Number of Pages in This Submission	Attorney Docket Number	0756-2306

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment / Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Declaration and Power of Attorney <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input type="checkbox"/> Other Enclosures 1. 2. 3. 4. 5. 6.
Remarks <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 50-2280 for the above identified docket number.		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Eric J. Robinson, Reg. No. 38,285 Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, VA 20165
Signature	
Date	5-12-03

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This will acknowledge receipt of the following:

1. Transmittal Form (2 copies)
2. Amendment
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In re PATENT application of:

TAKETOMI ASAMI et al  
Serial No. 09/842,315  
Filed: 04/26/2001  
Due Date: 05/13/2003  
For: MANUFACTURING METHOD FOR SEMICONDUCTOR DEVICE

Docket: 0756-2306

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